



Request for Expedited Procedure  
Under 37 CFR § 1.116  
Group Art Unit: 2181  
Docket No.: J0658.0018

AF  
SR

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Jens Barrenscheen et al.

Application No.: 10/727,102

Confirmation No.: 4397

Filed: December 2, 2003

Art Unit: 2181

For: Arrangement comprising a first  
semiconductor chip and a second  
semiconductor chip connected thereto

Examiner: C. K. Lee

**AMENDMENT AFTER FINAL ACTION UNDER 37 C.F.R. 1.116**

MS AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**INTRODUCTORY COMMENTS**

In response to the Office Action dated June 25, 2007, finally rejecting claims 2-4, 6-20, and 22-25, please amend the above-identified U.S. patent application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 8 of this paper.

**Remarks/Arguments** begin on page 9 of this paper.

**FEE CALCULATION**

Please Do Enter  
C.K.L. 8/24/2007